



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In reapplication of:

KONNO et al.

Serial No.: 07/743,383

Group Art Unit: 1104

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GPM
7-16-93

Filed: August 21, 1991

Examiner: G. Goudreau

For: METHOD FOR PRODUCING SEMICONDUCTOR INTEGRATED CIRCUITS
AND APPARATUS USED IN SUCH METHOD

REQUEST FOR STATUS

Honorable Commissioner
Washington, D. C. 20231

July 8, 1993

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GROUP 110
44-134

Sir:

On January 7, 1993, a response was filed in the Patent and Trademark Office. To date, no communication has been received in our office.

In view of the above, it is respectfully requested that the undersigned be provided with the current status of the subject application as soon as possible.

In the event that any fees are due with respect to this paper, please charge our Deposit Account No. 14-1060.

Respectfully submitted,

NIKAIDO, MARMELSTEIN, MURRAY & ORAM

Juan Carlos A. Marquez
Attorney for Applicants
Reg. No. 34,072

Atty. Docket No.: P698-1333

Metropolitan Square
655 15th Street, N. W.
Suite 330 - G Street Lobby
Washington, D. C. 20005-5701
Tel (202) 638-5000
Fax (202) 638-4810
JCM:myw